

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: FUJIWARA et al.
Serial No.: 09/913,896
Filed: August 21, 2001
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND PROCESS FOR MANUFACTURING THE SAME
Group: 2811
Examiner: O. Nadav

#8 / response
9-10-02
501.40536X00

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RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner
for Patents
Washington, D.C. 20231

August 29, 2002

Sir:

In response to the restriction requirement dated July 22, 2002, applicants hereby elect the Group II claims 1-24 and 38 and 40-42 directed to a process of making a semiconductor device, without traverse.

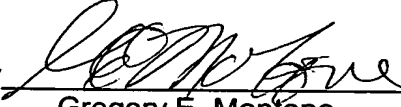
If the Examiner believes that there are any other points which may be clarified or otherwise disposed of, either by telephone discussion or by personal interview, the Examiner is invited to contact applicants' undersigned attorney at the number indicated below.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 501.40536X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By


Gregory E. Montone
Registration No. 28,141

GEM/kd
703/312-6600